

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Kobayashi

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Title: Method of Fabricating a
Semiconductor Device

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Group Art Unit (of Parent Application): 1763

Examiner: N/A

PRELIMINARY AMENDMENT

5 Assistant Commissioner for Patents
Box Patent Application
Washington, D.C. 20231

Dear Sir:

Entry of the following amendments is respectfully requested.

37 C.F.R. §1.10

I hereby certify that this is being deposited with the U.S. Postal Service "Express Mail Post Office to Addressee" service under 37 CFR § 1.10 on the date indicated below and is addressed to:

Assistant Commissioner for Patents
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